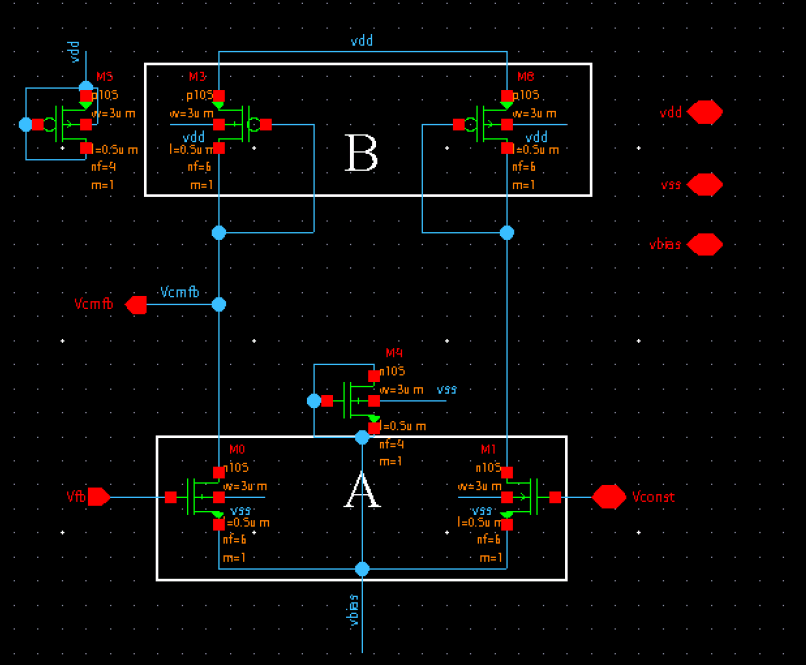
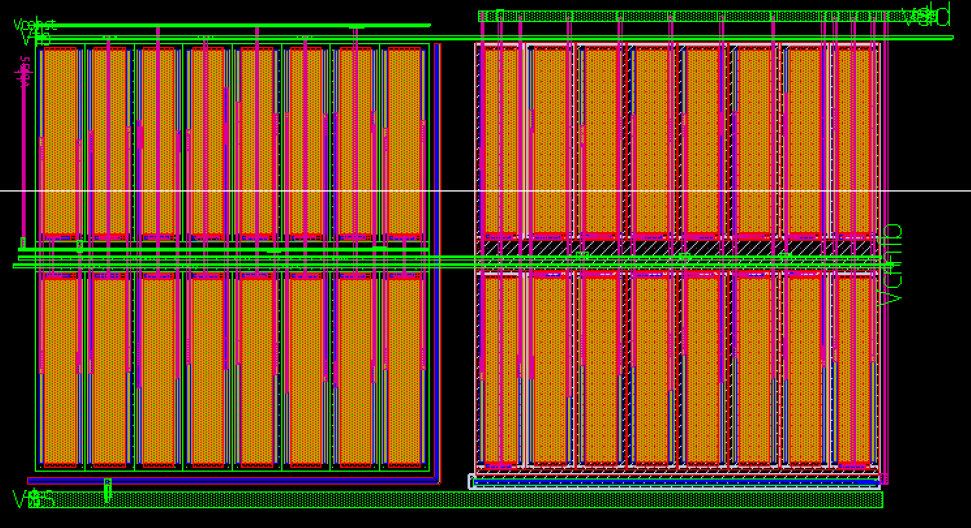
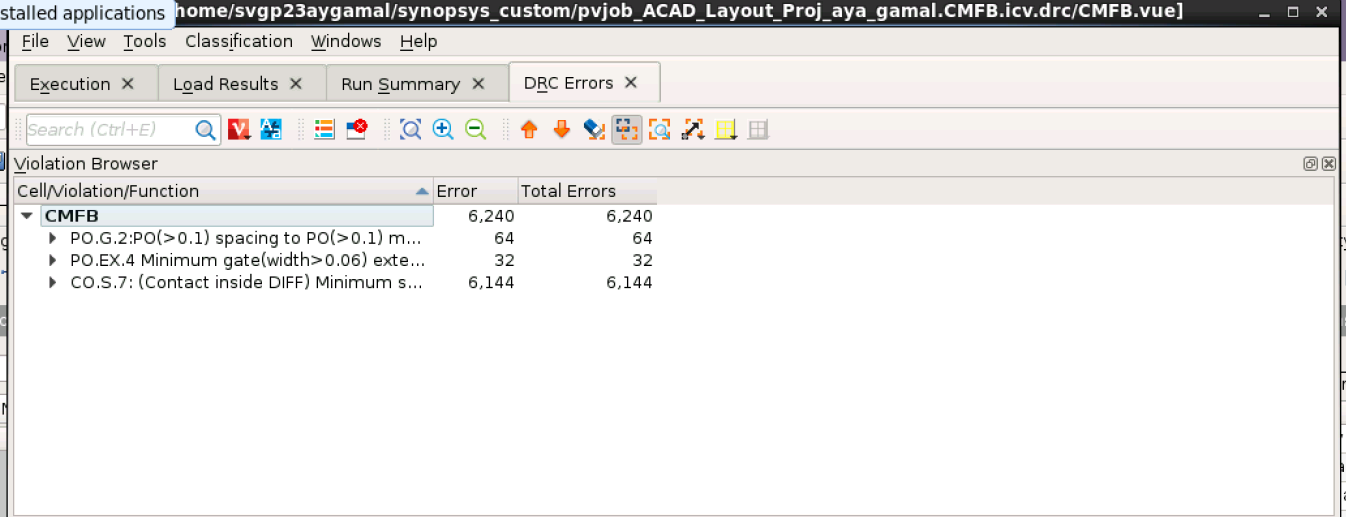
CMFB schematics



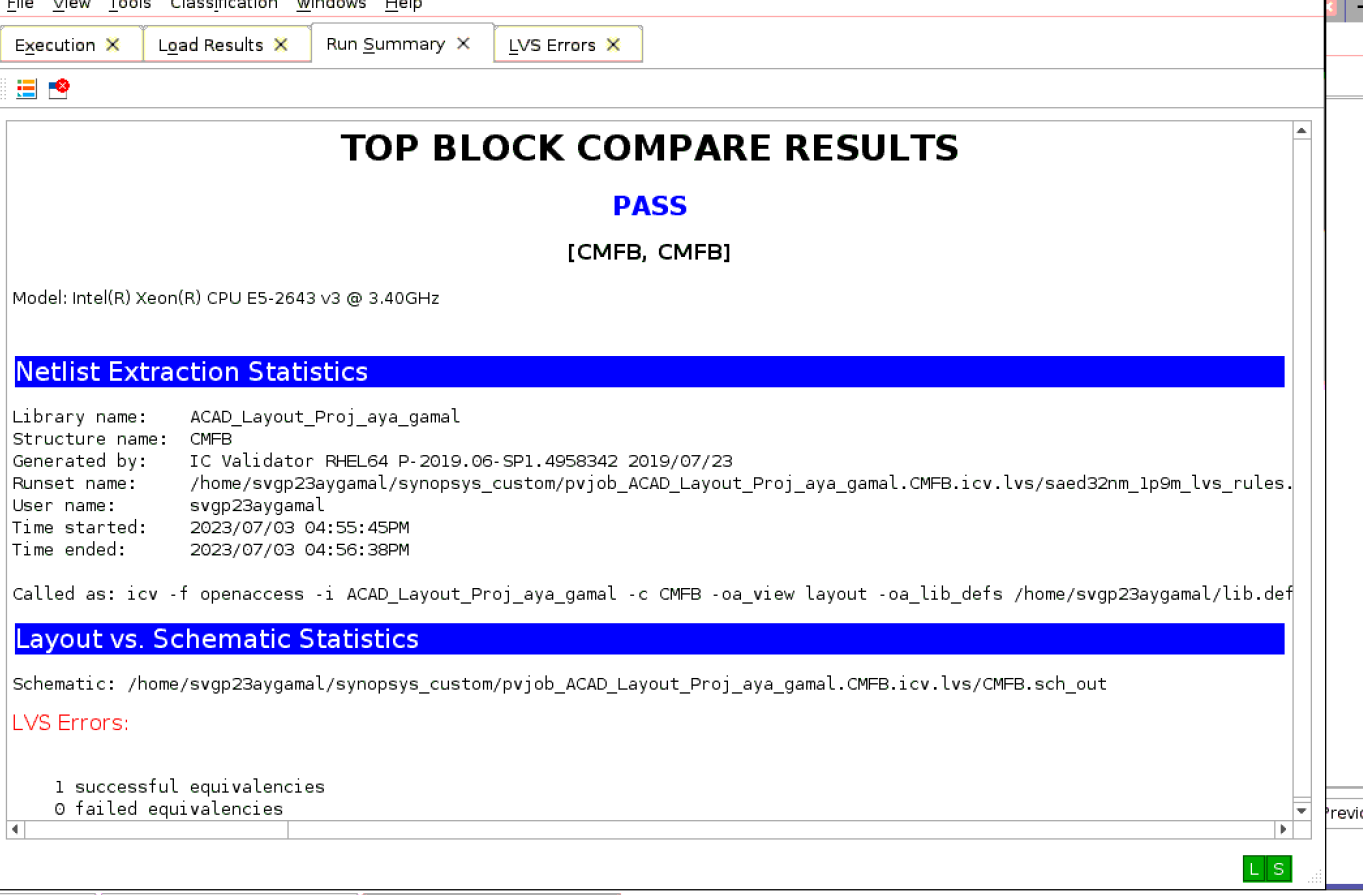
CMFB layout



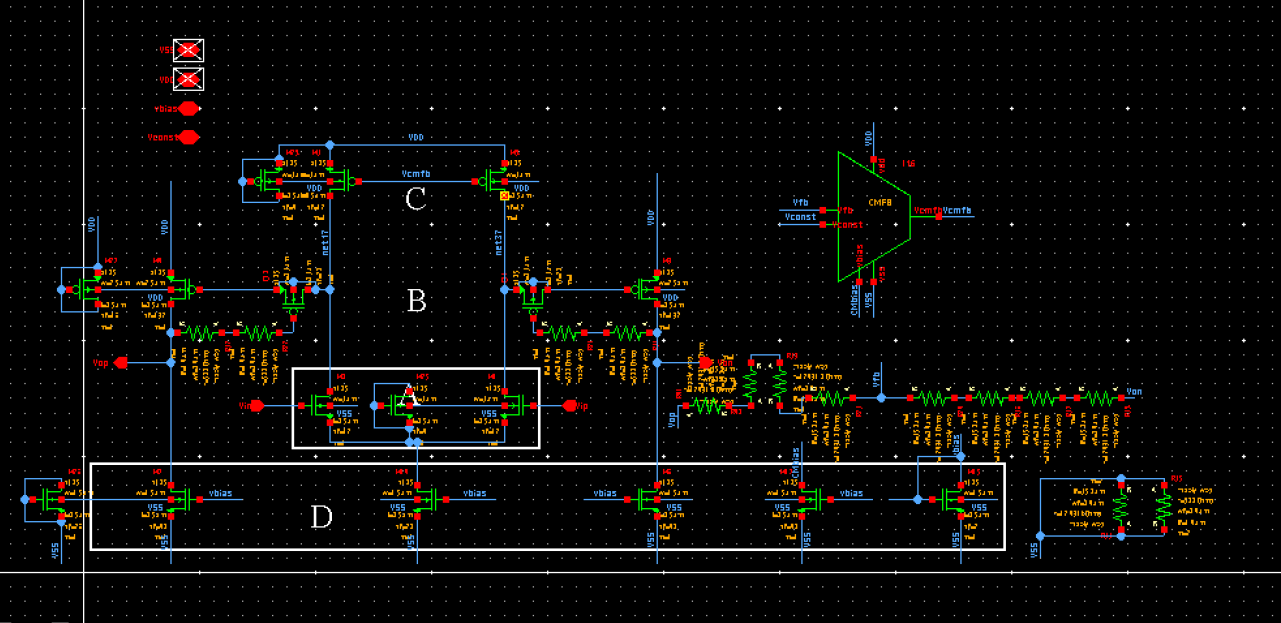
DRC run



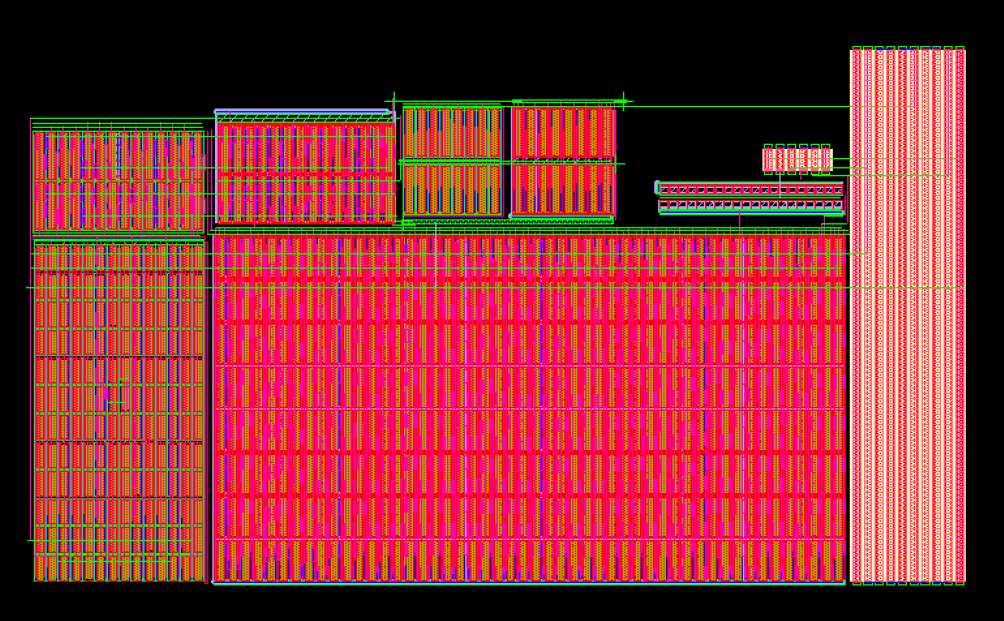
LVS run



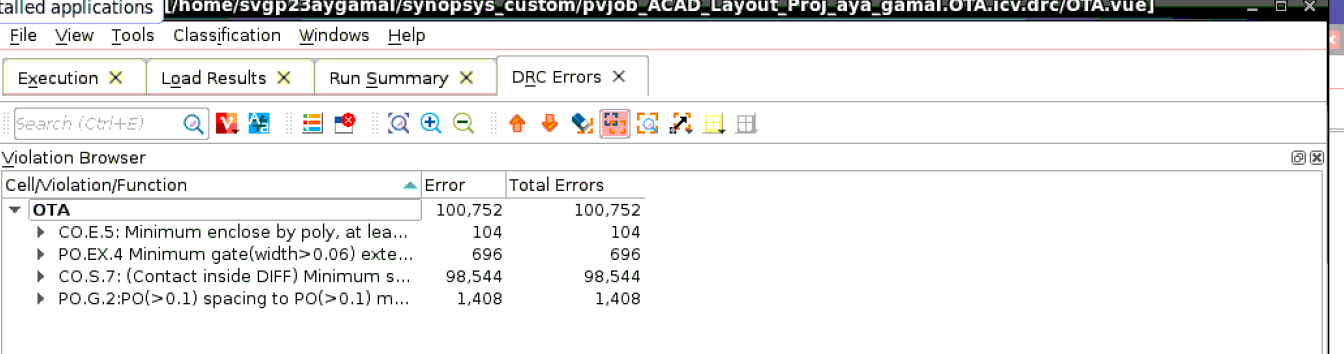
OTA schematic



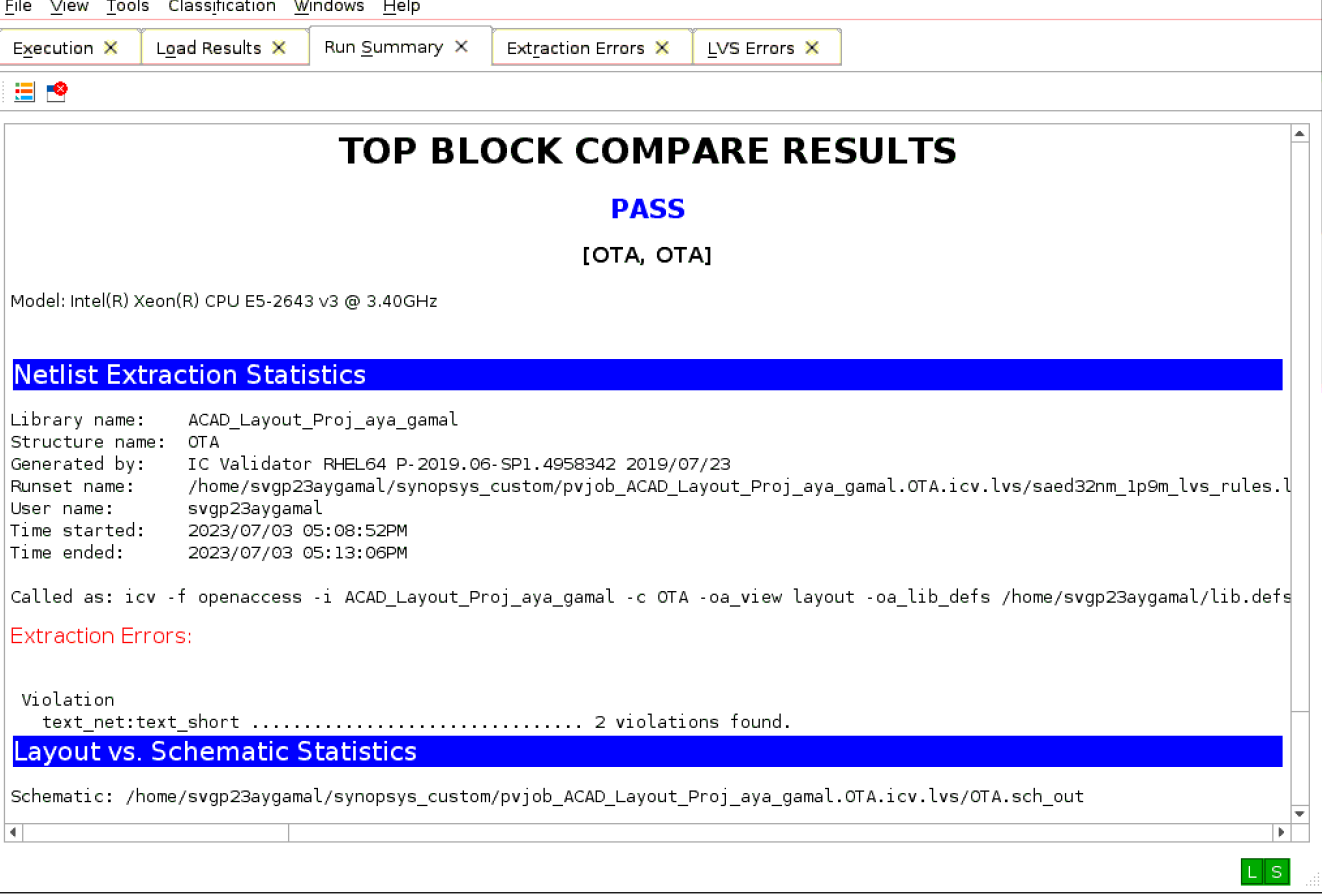
OTA layout



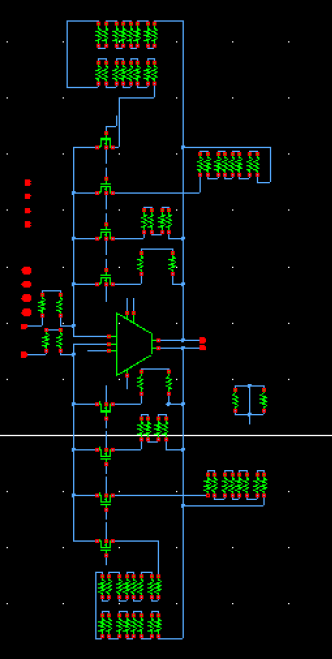
DRC run



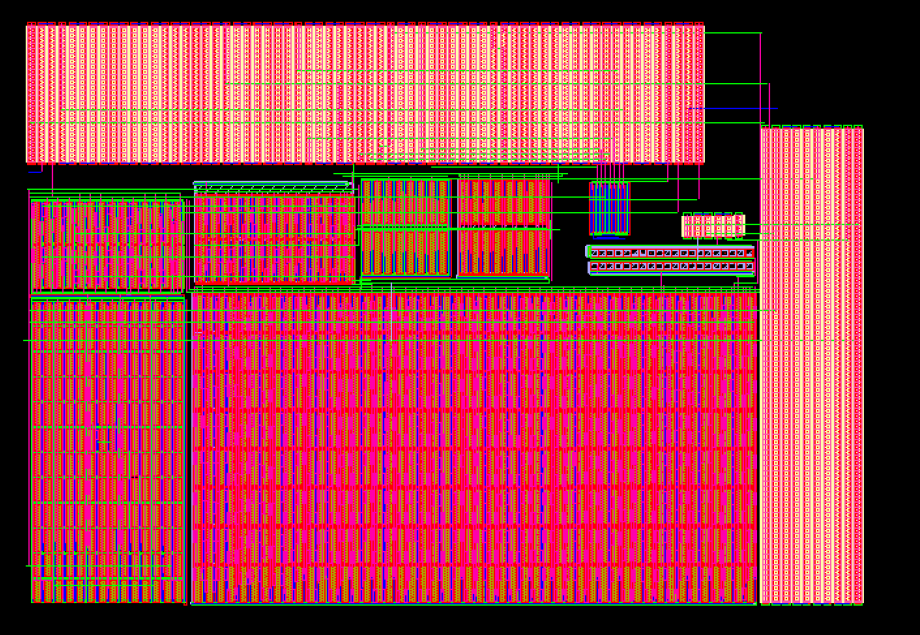
LVS run



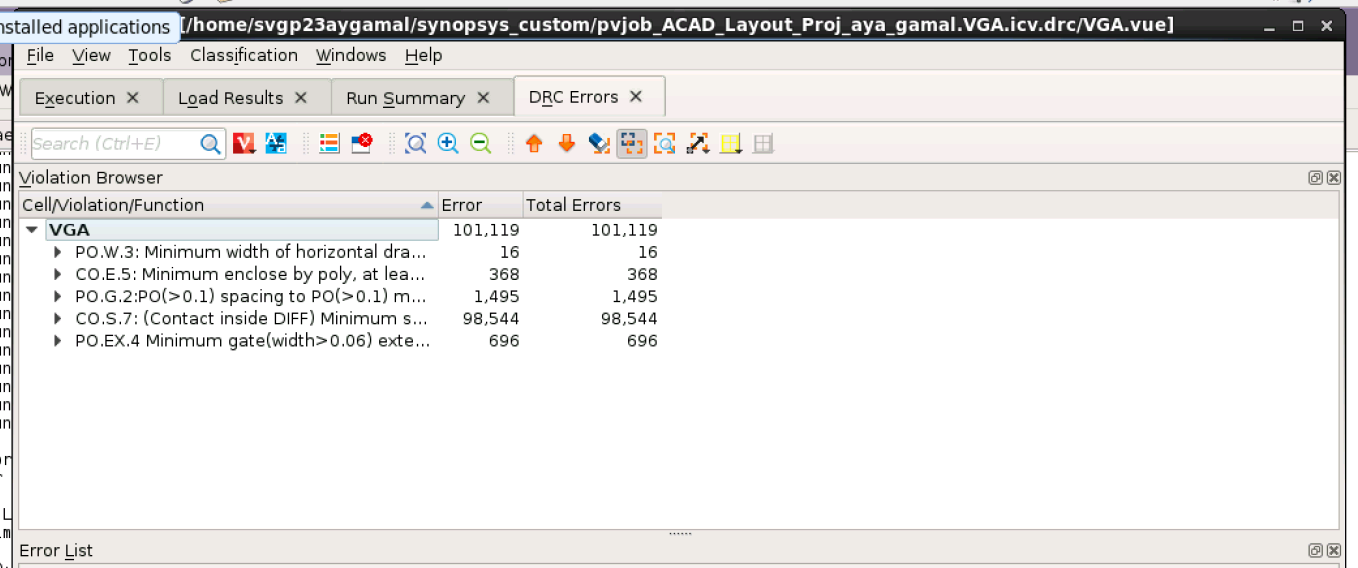
VGA schematic



VGA layout



DRC run



LVS run

